In <u>Claim 25</u>, "printed circuit wiring board design program comprising, in order" [lines 1-2] was changed to --computer-readable medium having stored thereon instructions which, when executed by a computer, perform steps--; and

--, comprising-- was inserted after "planes" [line 3, second occurrence].

In <u>Claim 26</u>, "printed circuit wiring board design program comprising, in order" [lines 1-2] was changed to --computer-readable medium having stored thereon instructions which, when executed by a computer, perform steps--;

--, comprising-- was inserted after "planes" [line 3, second occurrence]; and "part" [line] was changed to --step--.

In <u>Claim 27</u>, "printed circuit wiring board design program" [line 1] was changed to --computer-readable medium--.

In <u>Claim 28</u>, "printed circuit wiring board design program" [line 1] was changed to --computer-readable medium--.

In <u>Claim 29</u>, "printed circuit wiring board design program comprising, in order" [lines 1-2] was changed to --computer-readable medium having stored thereon instructions which, when executed by a computer, perform steps--; and

--, comprising-- was inserted after "planes" [line 3, second occurrence].

In <u>Claim 30</u>, "printed circuit wiring board design program comprising, in order" [lines 1-2] was changed to --computer-readable medium having stored thereon instructions which, when executed by a computer, perform steps--; and

--, comprising-- was inserted after "planes" [line 3, second occurrence].

Allowable Subject Matter

Claims 1-30 are allowed.

The following is an examiner's statement of reasons for allowance: although the prior art of record discloses various systems and methods for printed circuit wiring board design, the prior art of record does not particularly disclose, teach, or suggest, as per claims 1-3, 6-8, 11-13, 16-18, 21-23, 26-28, distance comparison and warning generation as claimed in combination with all of the features recited in their totality; as per claims 4, 9, 14, 19, 24, 29, circle creation, capacitor check, and warning generation as claimed in combination with all of the features recited in their totality; as per claims 5,